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**OFFICIAL**

Re: U.S. Utility Patent Application  
Appl. No. 09/188,708; Filed: November 10, 1998  
For: RISC Microprocessor Architecture Implementing Multiple Typed  
Register Sets  
Inventors: Garg et al.  
Our Ref: SP018.C4

RECEIVED  
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Sir:

Transmitted herewith for appropriate action are the following documents:

1. A Third Preliminary Amendment; and
2. Terminal Disclaimer to Obviate a Double Patenting Rejection under 37 C.F.R. §1.321(c).

In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

Assistant Commissioner for Patents  
November 1, 1999  
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The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036. A duplicate copy of this letter is enclosed.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.



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Enclosures

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